



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE
(UGC - AUTONOMOUS)

(Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi)
An ISO 9001: 2008 Certified Institution; www.mits.ac.in

Examination Section

Schedule - M.Tech – I Year I Semester (R14) Supplementary End Semester Examinations September – 2015

Academic Year – 2014-15

TIMINGS: RN: 02:00 PM to 05:00PM

DATE / DAY	Electrical Power Systems (EPS)	Solar Power Systems (SPS)	Machine Design (MD)	Digital Electronics & Communication Systems (DECS)	VLSI & Embedded Systems (VES)	Computer Science & Engineering (CSE)
15.09.2015 Tuesday	Modern Control Theory 14EPS11T01	Modern Control Theory 14SPS11T04	Advanced Mechanical Engineering Design 14MD11T01	Structural Digital Systems Design 14DECS11T01	Structural Digital Systems Design 14VES11T03	Programming Using Python 14CSP11T01
18.09.2015 Friday	Operation and Control of Power System 14EPS11T03	Power Electronic Converters 14SPS11T02	Fatigue, Creep & Fracture Mechanics 14MD11T03	Digital Communication Techniques 14DECS11T03	Embedded System Concepts 14VES11T02	Computer System Design 14CSP11T03
21.09.2015 Monday	Integration Of Renewable Energy Sources 14EPS11T04	Solar PV Technology 14SPS11T03	Computational Methods 14MD11T04	Advanced Digital Signal Processing 14DECS11T04	Analog & Digital IC Design 14VES11T04	Advance Databases 14CSP11T04
23.09.2015 Wednesday	Analysis of Power Electronic converters 14EPS11T05	Energy Storage in Solar Power System 14SPS11T05	Theory of Elasticity & Plasticity 14MD11T05	Adaptive Signal Processing 14DECS11T05	Hardware Description Languages 14VES11T05	Object Oriented Analysis Design and Design Pattern 14CSP11T05

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3. The File.

Notification date: 07.08.2015


[I/c] Controller of Examinations

